Listing of Claims:

Claim 1 (Previously Presented): A machine-executable method comprising:

executing sequences of instructions on a machine, the executed sequences of instructions causing the machine to perform the actions of,

reading a test file having a plurality of test vectors;

determining a required memory needed to execute the plurality of test vectors; and

using the required memory to estimate a cost to execute the test vectors.

Claim 2 (Previously Presented): The method of claim 1, wherein the executed sequences of instructions further cause the machine to perform the action of receiving a billing scheme; wherein using the required memory to estimate a cost includes using the billing scheme to estimate the cost to execute the test vectors.

Claim 3 (Original): The method of claim 1, wherein determining a required memory comprises determining a required memory needed for each of a plurality of boards of a tester to execute the test vectors for the board.

Claim 4 (Original): The method of claim 1, wherein determining a required memory comprises determining a required memory needed for each of a plurality of pins of a tester to execute the test vectors for the pin.

Claim 5 (Original): The method of claim 1, wherein determining a required memory comprises counting the number of test vectors for each of one or more tests in the test file.

Claim 6 (Original): The method of claim 1, wherein determining a required memory comprises:

determining a first memory requirement needed for a first pin of a tester to execute the test vectors for a first test in the test file;

setting the required memory equal to the first memory requirement; and

for each additional pin of the tester,

determining a second memory requirement needed for the additional pin to execute the test vectors for the first test; and

if the second memory requirement is greater than the first memory requirement, setting the required memory equal to the second memory requirement.

Claim 7 (Previously Presented): The method of claim 6, wherein the executed sequences of instructions further cause the machine to perform the action of, for each additional test in the test file:

for each pin of the tester, determining a third memory requirement for the pin to execute the test vectors for the additional test; and setting the required memory equal to the third memory requirement if the third memory requirement is greater than the required memory.

Claim 8 (Previously Presented): A system comprising:

a machine programmed to execute sequences of instructions, wherein execution of the sequences of instructions defines,

logic to read a test file having a plurality of test vectors and to determine a required memory needed to execute the plurality of test vectors; and

a billing predictor, communicatively coupled to the logic, to use the required memory to estimate a cost to execute the test vectors.

Claim 9 (Previously Presented): The system of claim 8, wherein the machine further comprises a user interface to display the cost to a user.

Claim 10 (Previously Presented): The system of claim 8, further comprising a tester having a plurality of boards; wherein the logic is to determine a required memory needed for each board of the tester to execute the test vectors for the board.

Claim 11 (Previously Presented): The system of claim 8, further comprising a tester having a plurality of boards, each board including a plurality of pins; wherein the logic is to determine a required memory needed for each pin to execute the test vectors for the pin.

Claim 12 (Original): The system of claim 8, wherein the logic is to determine the required memory by counting the number of test vectors for each test in the test file.

Claim 13 (Original): One or more machine-readable mediums having stored thereon sequences of instructions, which, when executed by a machine, cause the machine to perform the actions:

    reading a test file having a plurality of test vectors;

    determining a required memory needed to execute the plurality of test vectors; and

    using the required memory to estimate a cost to execute the test vectors.

Claim 14 (Previously Presented): The machine-readable mediums of claim 13, further comprising instructions, which when executed by the machine, cause the machine to perform the actions of receiving a billing scheme; and wherein the instructions for using the required memory to estimate a cost include instructions, which when executed by the machine, cause the machine to perform the actions of using the billing scheme to estimate the cost to execute the test vectors.

Claim 15 (Original): The machine-readable mediums of claim 13, wherein the instructions for determining a required memory comprise instructions, which

when executed by the machine, cause the machine to perform the actions of determining a required memory needed for each of a plurality of boards of a tester to execute the test vectors for the board.

Claim 16 (Original): The machine-readable mediums of claim 13, wherein the instructions for determining a required memory comprise instructions, which when executed by the machine, cause the machine to perform the actions of determining a required memory needed for each of a plurality of pins of a tester to execute the test vectors for the pin.

Claim 17 (Original): The machine-readable mediums of claim 13, wherein the instructions for determining a required memory comprise instructions, which when executed by the machine, cause the machine to perform the actions:

determining a first memory requirement needed for a first pin of a tester to execute the test vectors for a first test in the test file;

setting the required memory equal to the first memory requirement; and for each additional pin of the tester,

determining a second memory requirement needed for the additional pin to execute the test vectors for the first test; and

if the second memory requirement is greater than the first memory requirement , setting the required memory equal to the second memory requirement.